

**Notice of References Cited**

Application/Control No. 10/038,084

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Applicant(s)/Patent Under  
Reexamination  
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Examiner

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Art Unit

2814

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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf et al. Silicon Processing for the VLSI Era. Vol.1, Lattice Press, 1986, pp. 26-27, 36-37, 47-49 and 59-70.
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	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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